

FIG. 1A is a schematic diagram of a transistor circuit. A voltage source V_{source} (18) is connected to the source of a transistor (20) through a resistor (16). The gate of the transistor (20) is connected to a gate voltage V_{gate} (14). The drain of the transistor (20) is connected to a drain current I_D (12) and a drain voltage V_{ds} (10). The transistor (20) is shown with its internal structure, including the gate (22) and source (24).

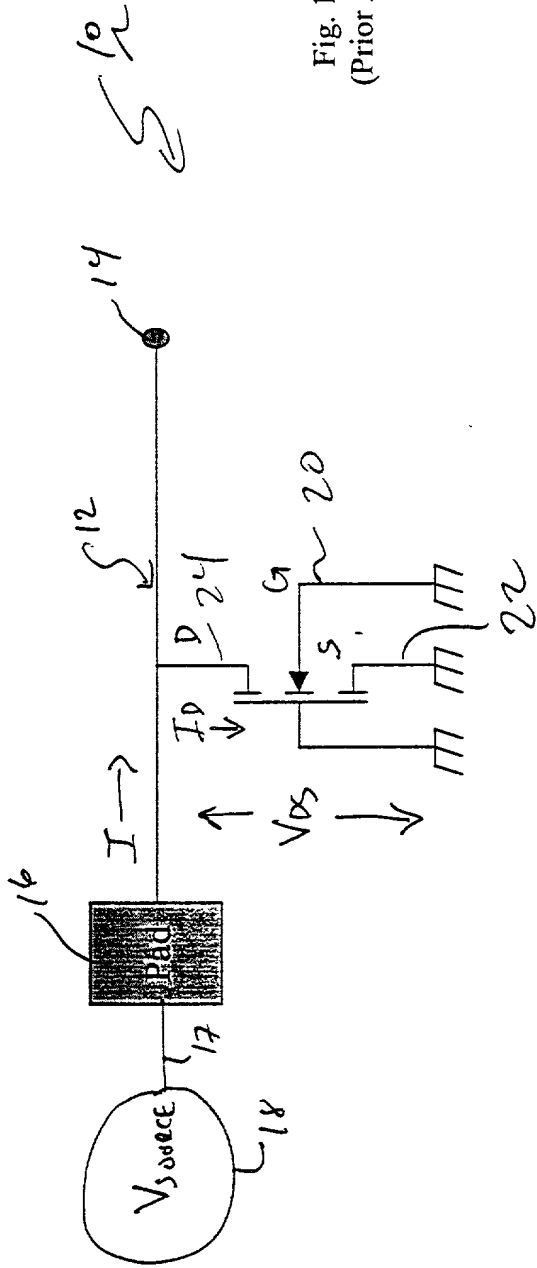


Fig. 1A
(Prior Art)

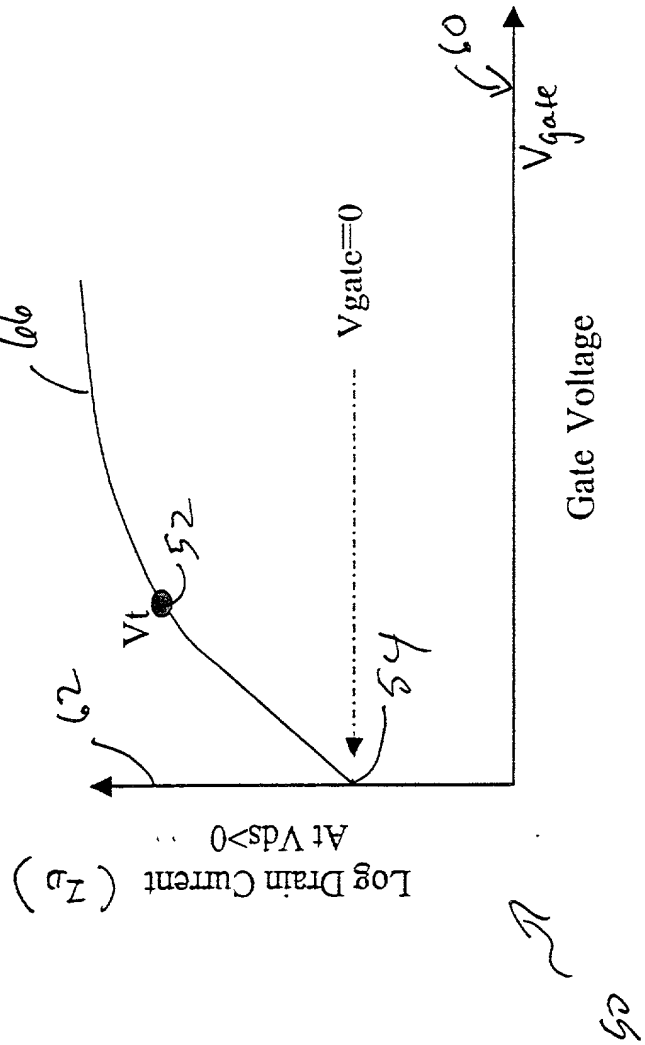


Fig. 1B
(Prior Art)

FIG. 1C

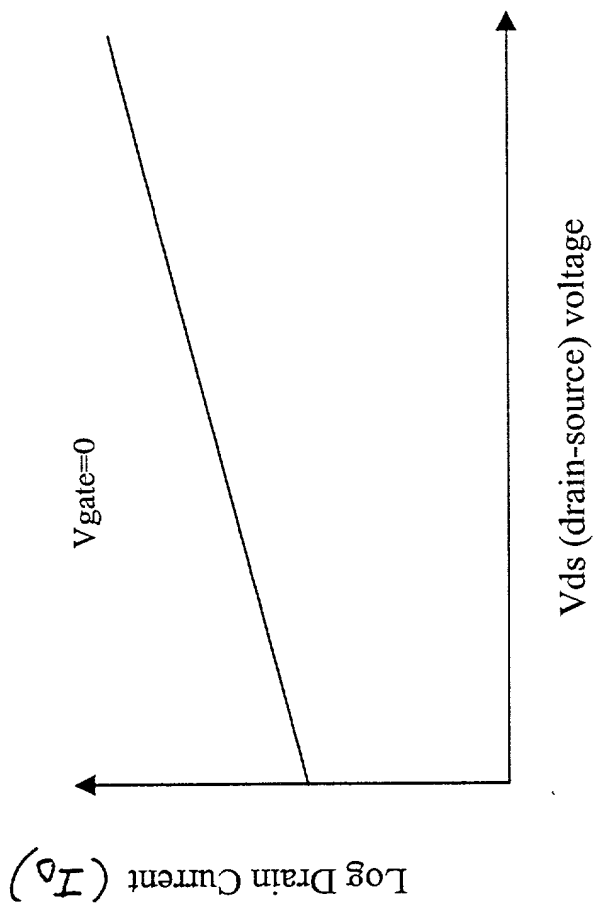


Fig. 1C
(Prior Art)

Fig. 2A

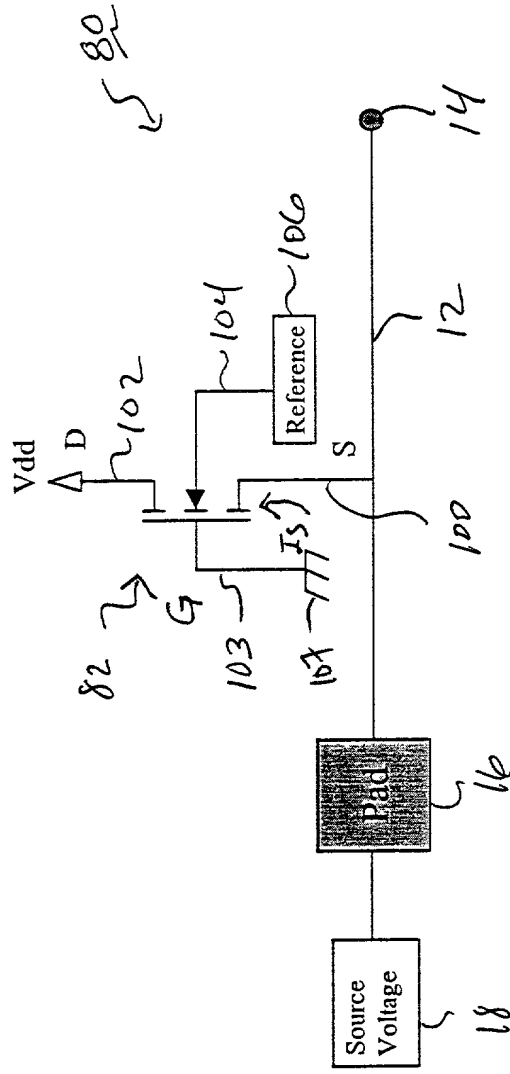
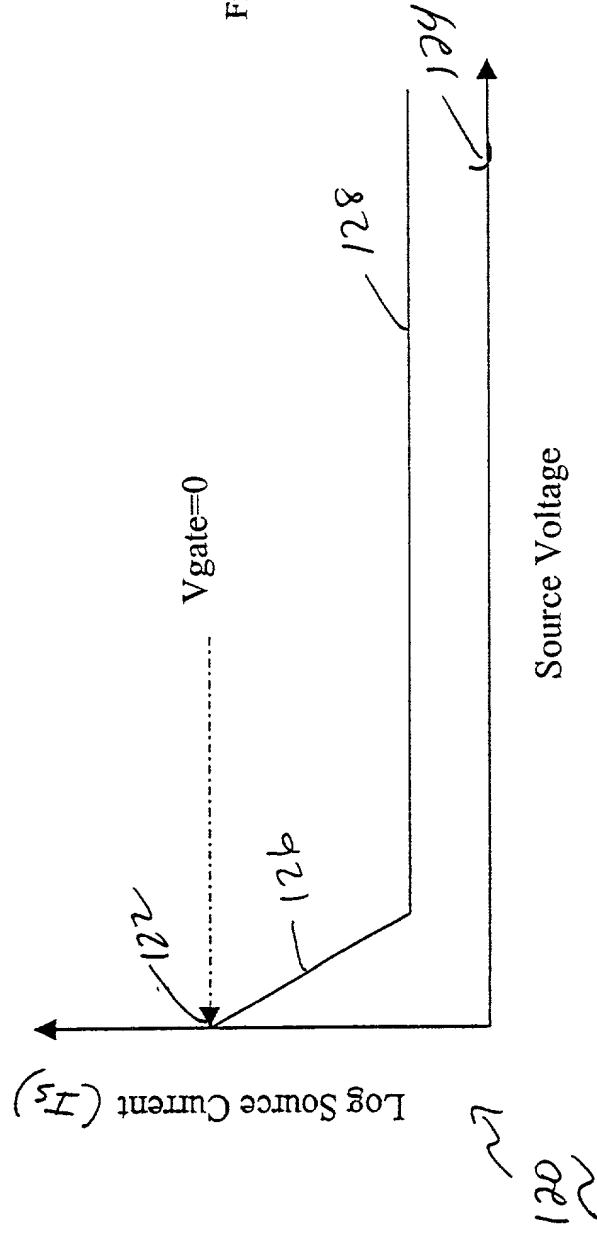


Fig. 2B



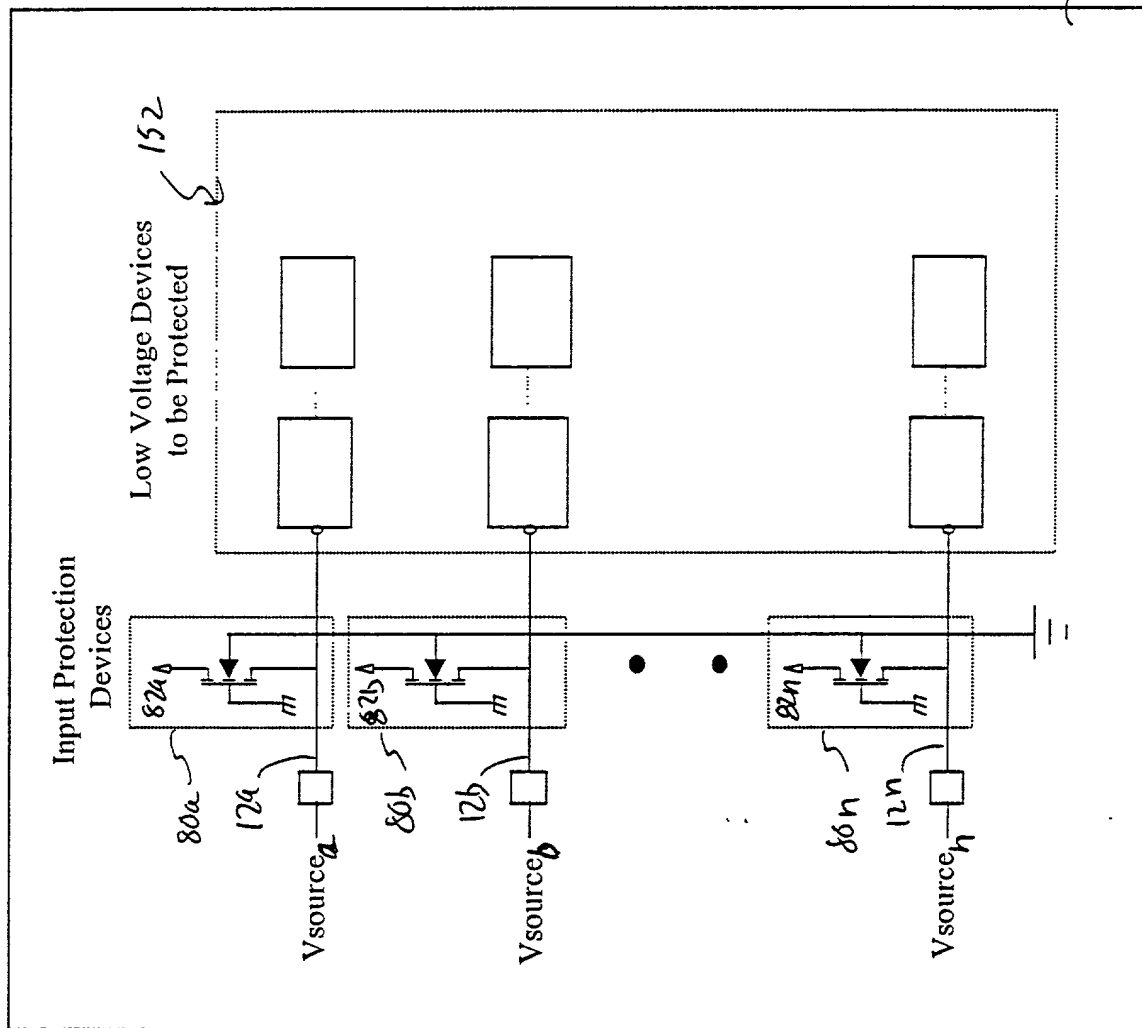


Fig. 3